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10/072,145

PB

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

7/21/03

FAX RECEIVED

In re Application of: Averett, Guy E. et al.
Serial No.: 10/072,145 JUL 16 2003
Filing Date: 02/07/2002 TECHNOLOGY CENTER 2800
Group Art Unit: 2811
Examiner: Magee, Thomas J.
Title: SEMICONDUCTOR DEVICE AND METHOD OF
PROVIDING REGIONS OF LOW SUBSTRATE
CAPACITANCE

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING FACSIMILE
TRANSMITTED TO THE PATENT AND TRADEMARK OFFICE, ATTN: EXAMINER
Thomas J. Magee AT THE FAX NO. (703) 872-9318

ON: July 16, 2003
Date of Facsimile Transmission
Lydia McNamara
Name of Person Signing Certificate
Lydia McNamara 7/16/03
SIGNATURE DATE

Honorable Commissioner of Patents,
P.O. BOX 1450
Alexandria, VA 22313-1450

AMENDMENT

SIR:

In response to the office action mailed March 28, 2003,
please amend the above-identified application as follows.

07/21/2003 PBRITTON 0000007 501086 10072145

01 FC:1251 110.00 DA